

## Release history for PM5341

Release Version	Date	OS Version	RS version	Language Version
1.64	01/03/2017	1.6.4	1.0.7	2.0.0
1.62	7/07/2016	1.6.2	1.0.7	2.0.0
1.60	14/05/2015	1.6.0	1.0.7	2.0.0
1.47	4/11/2014	1.4.7	1.0.6	2.0.0
1.22	18/03/2014	1.2.2	1.0.5	1.0.3
1.22	20/12/2013	1.2.2	1.0.3	1.0.3
1.20	30/10/2013	1.2.2	1.0.3	1.0.3

Revision	Changes Since Version	Change Details
1.64	1.62	<b>Official release of the changes implemented in revision 1.62</b>  <b>Fixes</b> <ul style="list-style-type: none"> <li>8905 Bitmap Register Changed from UINT16 to UINT32</li> </ul>
1.62	1.60	<b>*This version not officially released to end customer</b>  <b>Enhancements</b> <ul style="list-style-type: none"> <li>Individual harmonics magnitude &lt;1% available</li> <li>CT primary / secondary ration data can take float values</li> </ul> <b>Fixes</b> <ul style="list-style-type: none"> <li>In case of negative energy flow, the energy displayed not correct : fixed</li> <li>Single/ Multiple coil read correction for DI,DO, Alarms</li> <li>8905 Bitmap Register Changed from UINT32 to UINT16</li> <li>Power Fail Reset Counter Not Getting Changed on HMI After Reset</li> <li>In 2VT configuration, unused phase value was showing 0. – Fixed</li> <li>Display RTC time and QR code time stamp not matching when GMT offset is added. - Fixed.</li> <li>Missing of 1st pulse in DO Alarm Counter and Setup Changed from ON -&gt; OFF Vice verse – Fixed</li> </ul>
1.60	1.47	<b>Enhancements</b> <ul style="list-style-type: none"> <li>4 additional communication registers to represent PF value in IEC and Lag/lead format . These are 2 float 32 registers (3192, 3194) and 2 INT16 registers (3196, 3197)</li> <li>Firmware update through download restricted to 10 ( MID requirement)</li> <li>Evenly spaced pulses similar to optical pulse output implemented for Digital output</li> </ul> <b>Fixes</b>

## PM5341 FW Releases

		<ul style="list-style-type: none"> <li>Individual harmonic values not accessible through communication fixed</li> <li>Spurious neutral current while phases carry balanced currents fixed</li> <li>Demand Fix to Correct the behavior in Command Sync and Clock Sync Mode</li> <li>DI/DO/Relay counter value not reset to zero by Global reset through HMI fixed</li> <li>DO pulses per K__h not able to set through HMI -fixed</li> </ul>
1.47	1.22	<b>Enhancement</b> <ul style="list-style-type: none"> <li>Added Float 32 energy registers to support both INT64 and Float 32 energy registers</li> <li>Added QR code functionality</li> </ul> <b>Fixes</b> <ul style="list-style-type: none"> <li>Fix issue: Multi tariff energy not retaining value on power cycle while Multi tariff in Input mode</li> <li>Fix issue: RTC day roll over not in sync with 24 hours.</li> <li>Fix issue: After data log roll over value only partial records are retrievable</li> <li>Fixed Issue : Set IP Parameters Address(1501), Set Default IP Param (1502) Earlier Not working Properly</li> <li></li> </ul>
1.22	1.22	<b>Enhancements</b> <ul style="list-style-type: none"> <li>Flash chip lock enabled</li> <li>ICT support related enhancements</li> </ul>
1.22	1.20	<b>Fixes</b> <ul style="list-style-type: none"> <li>DLF upgrade can be done at 38400 baud rate</li> </ul>
1.20	None	Initial MID Ethernet Release Features : Pulse lock and Revenue lock , Tianma driver support, 61557-12 compliance